

REMARKS

In the office action, Figure 13 was objected to as not matching the description on the specification on page 13. The specification has been amended accordingly to reflect that the graphs are for Figure 15.

The specification was objected to as to how a formulation calculation or measurement for obtaining an impulse response is made and claims 1-6 were rejected under 35 U.S.C. §112 (first paragraph) as to not disclosing how a formulation or calculation or measurement of an impulse responsive multipath components is obtained. With respect to the generation of an impulse response, the analysis of an impulse response is known in the art. Concurrently filed herewith is an Information Disclosure Statement. One of the cited exerts is pages 63-65 in "Modern Digital and Analog Communication Systems" by D.P. Lathi. On pages 63-65 a unit impulse train is described and on page 64 graphs are shown of a unit impulse response produced by sampling at an interval of T_s . A further implementation of a circuit for making a channel impulse response is disclosed in U.S. Patent No. 5,799,010, where such a circuit is shown in Figures 3C and 6. With respect to Figure 6, the corresponding disclosure in that patent is at column 32 lines 35 to column 33 lines 18 where an adaptive vector correlator for use in estimating a channel impulse response is described. Accordingly, producing an impulse response and analyzing it is well known in the art.

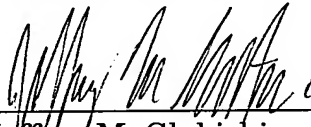
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Application No.: 10/074,398

Claims 1-7 were rejected under 35 U.S.C. §112 (second paragraph). Revisions to those claims as suggested by the Examiner have been made.

Reconsideration and entry of this amended is respectfully requested.

Respectfully submitted,

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Enclosures (4)